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Bib Data Sheet

**CONFIRMATION NO. 5865** 

SERIAL NUMBER 09/506,033	FILING DATE 02/16/2000 RULE	CLASS 345	GROUP ART	1 '	ATTORNEY DOCKET NO. 49087-CIP(820)					
APPLICANTS  Yasushi Kubota, Sakurai-shi, JAPAN; Hajime Washio, Tenri-shi, JAPAN; Ichiro Shiraki, Tenri-shi, JAPAN; Kazuhiro Maeda, Tenri-shi, JAPAN; Yasuyoshi Kaise, Tenri-shi, JAPAN;  *** CONTINUING DATA **********************************										
Foreign Priority claimed 35 USC 119 (a-d) condition met Verified and	Allowance	er COUNTRY JAPAN	SHEETS DRAWING 76	TOTAL CLAIMS 60	INDEPENDENT CLAIMS 3					
TITLE  Latch circuit, shift register circuit, logical circuit and image display device operated with a low consumption of power										
RECEIVED No	S: Authority has been giv to charge/cre for following:	rge/credit DEPOSIT ACCOUNT (time )								





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## Patent and Trademark Office

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SERIAL NUMBE 09/506,033	ER	FILING DATE 02/16/2000 RULE _		LASS 257 349	GROUP ART UNIT		_	ATTORNEY DOCKET NO. 49087-CIP(820)		
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Foreign Priority claimed 35 USC 119 (a-d) cond met Verified and Acknowledged	ditions	yes no no  Met a  Allowance  J.L  Iminer's Signature		STATE OR COUNTRY JAPAN	1	EETS AWING 76	TOTAL CLAIMS 60	F		
ADDRESS  — Dike Bronstein Roberts & Cushman LLP 130 Water Street Boston ,MA 02109  TITLE Latch circuit, shift register circuit, logical circuit and image display device operated with a low consumption of power										
FILING FEE F	۷o	S: Authority has been g to charge/c for following	redit DEPOSIT ACCOUNT			All Fees  1.16 Fees (Filing)  1.17 Fees (Processing Ext. of time)  1.18 Fees (Issue)  Other  Credit				